

<b>Notice of References Cited</b>	Application/Control No. 10/575,381	Applicant(s)/Patent Under Reexamination GSCHOSSMANN ET AL.	
	Examiner DHARTI H. PATEL	Art Unit 2836	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-2004/0016461	01-2004	Qu et al.	137/554
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
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**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
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	S					
	I					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
*	U	Morsch, Joachim; Circuit arrangement for protecting electronic circuits against overvoltages; pages 1-2; Fig. 1 and 2
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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